Guide to the Rob Walker Papers M0742

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Title: Rob Walker papers
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Abstract: Rob Walker (1935-2016) was a Silicon Valley native and Silicon Valley educated electrical engineer who was involved with semiconductors since the 1960s at Fairchild, Intel and as a founder of LSI Logic. He also founded the Silicon Genesis oral history series. His collection contains presentation slides, reports, sales material, and other documentation from from LSI Logic Corp.; articles and papers given by him; documents from Fairchild Semiconductor; specifications from Lawrence Livermore Laboratories; and documentation from Philco Corp.

Arrangement
Arranged into five series: Series 1: Fairchild Semiconductor materials; Series 2: LSI Logic Corp. materials; Series 3: Lawrence Livermore Laboratory materials; Series 4: Philco Corp. materials; and Series 5: Audiovisual materials.

Scope and Content of Collection
The collection contains manuals, binders of research materials, documents, and minutes of meetings from Fairchild Semiconductor; materials documenting Walker's career at LSI Logic, including correspondence, presentation slides, reports, publications put out by the Corporate Communications Department, as well as articles and papers by Rob Walker and by others; technical data from Walker's research at Lawrence Livermore Laboratories in the 1960's; technical manuals, marketing materials, and other documents accumulated during Walker's years at Philco Corp.; and an audiocassette of a 1995 interview with Walker.

Biography / Administrative History
Walker is an ASIC (Application specific integrated circuits) consultant and founder of LSI Logic. He is also the author of SILICON DESTINY: THE STORY OF ASIC AND LSI LOGIC CORPORATIONS, CMC Publications (Milpitas, Calif.), 1993.

Acquisition Information

Preferred Citation
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Conditions Governing Access
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Subjects and Indexing Terms
Computer industry.
Application specific integrated circuits.
Microelectronics industry -- California -- Santa Clara County.
LSI Logic Corporation.
Fairchild Camera and Instrument Corporation

Series 1. Fairchild Semiconductor materials 527469 1966-1972
Series Scope and Content Summary
This series contains manuals, binders of research materials and documents, articles and papers by Rob Walker, and minutes of Fairchild meetings.
Series 1. Fairchild Semiconductor materials 5274691966-1972

Guide to the Rob Walker Papers

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Box 1, folder 14: PPC meeting minutes - Fairchild MOS, 527475 1968 July 17
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Series 2. LSI Logic Corp. materials 527099 1982-1994

Scope and Contents note
Includes extensive memos on LSI's Modular Design Environment.

Correspondence and memos, Subseries 2.1527441 1981-1990

Subseries Scope and Content Summary
This subseries contains correspondence and memos to and from Rob Walker relating to LSI Logic's project, products, and goals.

Box 1, folder 17: Correspondence re: litigation -- Lear Siegler, Inc. vs. LSI Logic Corp., 527467 1982 Jan. 12
Box 1, folder 18: Correspondence, 527465 1983
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Box 2, folder 6: Memos, 527449 1987

Scope and Contents note
Includes extensive memos on LSI's Modular Design Environment.

Box 2, folder 7: Memos, 527447 1988
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Box 2, folder 9: Memos, 527443 undated

Presentation materials, Series 2.2527259 1981-1990

Subseries Scope and Content Summary
This series is almost entirely comprised of photocopies of slide cells for talks that Rob Walker gave while at LSI Logic to both internal and external audiences.

Box 2, folder 10: Logic array customers foil, 527439 1981 Mar. 25
box 2, folder 11  Advantages of the phased ILC approach, 527437 1981 July 30
box 2, folder 12  LSI design philosophy, 527435 1981 Nov.
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This subseries contains reports, publications, press releases, and other documents put out by the Corporate Communications Department of LSI Logic.

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Subseries Scope and Content Summary

This subseries contains technical information and specifications produced by LSI Logic Corp. in its research and development.

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- box 5, folder 8  Series 7000 2 micron CMOS macrocell arrays, 527197 1982
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- box 5, folder 11  Circuit photograph, 527191 undated

**Sales and marketing materials,** Subseries 2.6527165 1983 - 1989

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- box 5, folder 12  Product brochures, 527187 undated
- box 5, folder 13  Short form catalog, 527185 1988 Nov.
- box 5, folder 14  Various data sheets, etc., 527183 undated
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- box 5, folder 18  ECL array sales bits, 527175 1983
- box 5, folder 19  SPARCstation and LSI Logic, 527173 1989
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Conditions Governing Access note

Note: "Confidential" stamped on front of document.
Papers and articles by Rob Walker, Subseries 2.7527133 1981-1994

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This subseries contains papers and articles written by Rob Walker while at LSI Logic Corp.

box 6, folder 1  LSI gate arrays outpace standard logic / Robert Hartmann & Rob Walker, 527163 1981 Mar. 5
box 6, folder 2  Logic arrays, technologies, and design / Rob Walker, 527161 1981
box 6, folder 3  Converting SSI/MSI and PLA designs to CMOS logic arrays / Rob Walker, Richard Derickson, & Keith R. Lobo, 527159 1982
box 6, folder 4  Partitioning systems into logic arrays / Rob Walker, 527157 1982
box 6, folder 5  A systems approach to logic arrays / Rob Walker & Don Snoderman, 527155 1982
box 6, folder 6  Logic design for CMOS logic arrays / Rob Walker & Richard Derickson, 527153 1983 Feb.
box 6, folder 7  Gate arrays and standard cell / Rob Walker, 527151 1983 Mar.
box 6, folder 8  LSIM / ZSIM paper / Rob Walker, 527149 1984
box 6, folder 9  Technology upstarts rock ASIC market / Rob Walker. -- In Electronic Engineering Times, 527147 1994 Nov. 14
box 6, folder 10  ASIC test / Rob Walker, 527145 undated
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Papers and articles by other LSI employees, Subseries 2.8527125 1981

Subseries Scope and Content Summary
This subseries contains papers and articles written by colleagues of Rob Walker at LSI Logic Corp.

box 6, folder 16  Current trends and future capability of LSI / W.J. Corrigan, 527131 undated
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box 6, folder 18  A development system for logic arrays / James S. Koford and Edwin R. Jones, 527127 1981

Articles and clippings about LSI, Subseries 2.9527119 1981-1994

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This subseries contains articles and clippings from newspapers, magazines, and other publications LSI Logic Corp.

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Biographical information, Subseries 2.11527107 undated

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This subseries contains miscellaneous materials generated by LSI Logic Corp.

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box 6, folder 27  Read out for 256 PHA, 527097 1958
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box 7, folder 1  3D analyzer readout, PHA recordings and Min-Max selector, 527093 1960
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box 7, folder 3  Digital data procedures / Logic module, 527089 1962
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box 7, folder 5  Resistance measurement system, 527085 1963

Series Scope and Content Summary
This series contains technical manuals, marketing materials, and other documents generated by Philco Corp. It also contains a copy of the manuscript and the final published version of an article written by Rob Walker entitled: "A building block approach to digital-to-analog convertors."

box 7, folder 8  System 4-D digital logic building blocks. -- Palo Alto, Calif. : Philco. WDL Division [1 of 2], 527077 1966 Apr. 1
box 7, folder 9  System 4-D digital logic building blocks. -- Palo Alto, Calif. : Philco. WDL Division [2 of 2], 527075 1966 Apr. 1
box 8, folder 1  Optimized microelectronics packaging configuration. -- Palo Alto, Calif. : Philco. WDL Division, WDL-TP1860527073 1965 Apr. 2
box 8, folder 2  Phase 1 report Gemini terminal landing system data subsystem A201.BADA. -- Palo Alto, Calif. : Philco. WDL Division, 527071 1965 Dec. 23
box 8, folder 3  Mark II Avionics Systems. -- Rev. to Part III, section 3. -- Palo Alto, Calif. : Philco. WDL Division, WDL-TP21866A527069 1966 July 8
box 8, folder 4  A building block approach to digital-to-analog convertors / Rob Walker. -- In The Electronic Engineer, 527067 1968 May
box 8, folder 5  A building block approach to analog convertors, 527065 undated
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Rob Walker interview on KALW, 527051 1995 May 31
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